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|  | **DEPARTMENT OF COMPUTER ENGINEERING** |

**Experiment No. 07**

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| Semester | S.E-Semester III – Computer Engineering |
| Subject | Digital Logic and Computer Architecture |
| Subject Professor In-charge | Prof. Avinash Shrivas |
| Assisting Teachers | Prof. Avinash Shrivas |

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| Student Name – Deep Salunkhe |
| Roll Number – 21102A0014 |
| Division and Batch – Division A, Batch 1 |
| Date of Implementation – 14/09/2022 |
| Experiment TitleTo implement Multiplexer and Demultiplexer |
| **Theory:**  Multiplexer  The a multiplexer, also known as a data selector, is a device that selects between several analog or digital input signals and forwards the selected input to a single output line. The selection is directed by a separate set of digital inputs known as select lines  .  Demultiplexer  A demultiplexer (also known as a demux or data distributor) is defined as **a circuit that can distribute or deliver multiple outputs from a single input**. A demultiplexer can perform as a single input with many output switches |
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| **Implementation**  Multiplexer      **Demultiplerer** |
| Conclusion: |